

Notice of References Cited

Application/Control No.

09/870,585

Applicant(s)/Patent Under
Reexamination
PARK ET AL.

Examiner

Suresh K Suryawanshi

Art Unit

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	V	Jack Robertson, Group Supports Direct Rambus DRAM Alternative, 04/29/1999, TechWeb, page 1
	W	Preliminary publication of JEDEC Semiconductor Memory Standards, August 1999, pages 72
	X	JEDEC STANDARD, Double Data Rate (DDR) SDRAM Specification, June 2000, pages 72

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.